

X-805-8 US

PATENT

2132  
JFV

IN THE UNITED STATES PATENT OFFICE

Applicants: Stephen M. Trimberger et al.  
Assignee: Xilinx, Inc.  
Title: "BITSTREAM FOR CONFIGURING A PLD WITH ENCRYPTED DESIGN DATA"  
Serial No.: 09/724,734      File Date: November 28, 2000  
Examiner: Samson B. Lemma      Art Unit: 2132  
Docket No.: X-805-8 US      Conf. No.: 7773

Mail Stop AMENDMENT  
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These references were cited in related U.S. patent applications. Copies of these references have not been supplied herein since they were cited in the related applications. The enclosed reference are being cited after the receipt of a first office action but prior to any final action.

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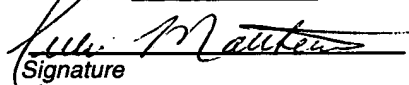
Respectfully submitted,

  
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408-879-4604

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PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

Substitute for form 1449A/PTO

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Sheet	1	of	1
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**Complete if Known**

<b>Application / Conf. No.</b>	09/724,734 / 7773
<b>Filing Date</b>	November 28, 2000
<b>First Named Inventor</b>	Stephen M. Trimberger
<b>Art Unit</b>	2132
<b>Examiner Name</b>	Samson B. Lemma
<b>Attorney Docket Number</b>	X-805-8 US

[illegible][illegible]

**Examiner  
Signature**

Date  
Considered

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